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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043362	FILING DATE 01/09/2002	CLASS 438	SUBCLASS 127	GAU 2827	EXAMINER ZARNETT
<b>**APPLICANTS:</b> Farnworth Warren;					
<b>**CONTINUING DATA VERIFIED:</b> THIS APPLICATION IS A DIV OF 09/640,801 08/17/2000 <i>now USPAT 6432752</i>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> <i>none</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>					
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no			ATTORNEY DOCKET NO		
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no			4212.1US (99-0306.1)		
Verified and Acknowledged Examiners's initials					
TITLE : Stereolithographic methods for fabricating hermetic semiconductor device packages and semiconductor devices including stereolithographically fabricated hermetic packages					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
Assistant Examiner			
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
Primary Examiner		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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